

Online Winter Faculty Development Programme Semiconductor Fabrication & Packaging Technology

January 20 - 31, 2025 : Online live streaming











UPI ID : iiitdmj@indianbl

Schedule and Course Contents

[January 20 - 31, 2025, 16:00 to 20:00 hours]

- Semiconductor fabrication- The process of manufacturing semiconductor devices, such as integrated circuits (ICs)
- CMOS Fabrication
- Crystal Growth & Cleaning
- Thermal Oxidation & Backend Technology
- Lithography & etching
- Diffusion & Ion Implantation
 Demonstration & Etablishing (DVD) CVD
- Deposition & Etching (PVD, CVD, PECVD)
 Consistent duration of the section of the sect
- Semiconductor bonding & packaging & testing- The process of protecting semiconductor devices and connecting them to the external environment
- Assembly and packaging
- Materials used in semiconductor packaging, such as ceramic and plastic
- Wire bonding or flip-chip bonding techniques used to connect components

IIITDM Jabalpur

Testing the packaged device to ensure it meets performance specifications

Coordinators	RESOURCE PERSONS
Dr. Dip Prakash Samajdar, Joint- Principal Coordinator, IIITDM Jabalpur Email: <u>dip.samajdar@iiitdmj.ac.in,</u> Mobile: 91-9477137992, Prof. Sanjeev Manhas and Prof. Arnab Dutta, Principal Coordinator, IIT Roorkee Email: <u>eict@iitr.ac.in</u>	 Prof. Sanjeev Manhas, IIT Roorkee Prof. Tanmoy Pramanik, IIT Roorkee Prof. Brijesh Kumar, IIT Roorkee Prof. Arnab Datta, IIT Roorkee Prof. Pradeep Dixit, IIT Bombay Dr. Navab Singh, Director MEMS, Institute of Microelectronics, Singapore
Registration Details	
Registration link – Please fill in the registration form using the following link: <u>https://forms.gle/WJ8vrckgp4V2ZeEv6</u> Registration fee: INR 500/- Last Date for Registration: January 17, 2025	Online payment details: • Internet banking Beneficiary Name: PDPM IIITDM Jabalpur Bank Name: INDIAN BANK A/C No. : 50018692852 IFSC Code: IDIB000M694
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